Amendments To The Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. (**currently amended**) A distributed base station system comprising:

a base band unit (BBU), ~~which comprises~~ **configured to comprise:**

a Main Processing & Timing unit **which comprises a main processing unit and a clock unit;[[,]]**

a base band signal processing unit; **[[,]]**

a transmission unit; **[[,]]** and

an interface unit, **configured to:** ~~for providing an interface for intercommunicating~~ **intercommunicate** data with an external unit; **[[,]]**

~~intercommunicating~~ **intercommunicate** digital base band signals with the base band signal processing unit; **[[, and]]**

~~intercommunicating~~ **intercommunicate** master control information with the ~~Main Processing & Timing unit~~ **main processing unit**;

wherein the interface unit comprises ~~one or~~ a ~~plurality of~~ primary base band Radio Frequency (RF) ~~interface(s)~~ **interface**; and the interface unit ~~being~~ **is** integrated with the Main Processing & Timing unit, the base band signal processing unit and the transmission unit; and

a Radio Frequency unit (RFU) **wherein the RFU** ~~which~~ comprises a secondary base band RF interface ~~thereon;~~ **and the RFU is connected to** ~~wherein~~ the primary base band RF interface of the BBU ~~is connected with the secondary base band RF interface of the RFU, and the BBU transmits uplink/downlink base band data and master controller state information with the RFU via the primary base band RF interface and the secondary base band RF interface~~.

2. (original) The system according to Claim 1, wherein the primary base band RF interface and the secondary base band RF interface both are high speed digital interfaces.

3. (**currently amended**) The system according to Claim 1, wherein the base station system comprises a plurality of BBUs, and the BBUs are interconnected with each other via wire cables or optical fibers; the interface unit of each BBU comprises: ~~one or~~ a ~~plurality of~~ primary capacity expansion ~~interface(s) for transmitting~~ **configured to transmit** synchronous clock signals, base band information, transmission information and the master control information among BBUs, **and** to achieve interconnection and data sharing among BBUs.

4. (original) The system according to Claim 3, wherein the primary capacity expansion interface comprises a primary capacity expansion interface that provides an active/standby switchover control signal.

5. (original) The system according to Claim 3, wherein the interface unit further comprises an identification interface for marking the type of the base station and the position of the BBU.

6. (original) The system according to Claim 3, wherein the interface unit further comprises a Dry Contact input interface for expanding the input Dry Contact functions of the base station.

7. (original) The system according to Claim 3, wherein the BBUs comprise a master BBU that works in an active state

8. (original) The system according to Claim 7, wherein the BBUs comprise a standby BBU that works in a standby state.

9. (original) The system according to Claim 8, wherein the RFU is connected with any one of the plurality of BBUs.

10. (original) The system according to Claim 7, wherein the BBUs comprise a slave

BBU that works in a slave state.

11. (original) The system according to Claim 3, further comprising: an exchange BB cassette with a plurality of secondary capacity expansion interfaces, and each BBU is connected with one of the secondary capacity expansion interfaces on the exchange BB cassette via the respective primary capacity expansion interface of the BBU.

12-38. (**canceled**)

39. (previously presented) A base band unit (BBU), comprising:

a Main Processing & Timing unit **which comprises a main processing unit and a clock unit**, ~~**for controlling**~~ **configured to control** a base station, ~~**exchanging**~~ **exchange** signals and traffic data among the units in the base station and ~~**providing**~~ **provide** clock signals;

a base band signal processing unit, ~~**for processing**~~ **configured to process** symbol-level and chip-level digital signals in physical layer;

a transmission unit**[[,]]** which is connected with a base station controller, **configured to intercommunicate** ~~**for intercommunicating**~~ data information between the base station and the base station controller; and

an interface unit **configured to** ~~**for intercommunicating**~~ **intercommunicate** with external data information; ~~**, intercommunicating**~~ **intercommunicate** digital base band signals with the base band signal processing unit ~~**, and intercommunicating**~~ **intercommunicate** master control information with the ~~**Main Processing & Timing**~~ **main processing** unit;

wherein the interface unit ~~**comprising**~~ **comprises a** ~~**one or a plurality of**~~ primary base band RF ~~**interface(s)**~~ **interface** ~~**for connecting with the RFU and transmitting uplink/downlink base band data and master controller state information with the RFU; a power supply interface for connecting with an external power supply; and a debugging interface for managing and maintaining the base station;~~ and

the interface unit is integrated with the Main Processing & Timing unit, the base band signal processing unit, **and** the transmission unit ~~**and the interface unit are integrated**~~.

40. (previously presented) The Base band unit according to Claim 39, wherein the primary base band RF interface is a high speed digital interface.

41. (**canceled**)

42. (previously presented) The Base band unit according to Claim 39, wherein the interface unit further comprises an identification interface for marking the type of the base station and the position of the BBU, and the identification interface is a DIP switch and /or a cable identification interface.

43-44. (**canceled**)

45. (**currently amended**) The Base band unit according to Claim 39, wherein the interface unit further comprises: a capacity expansion interface**, configured to transmit** ~~**for transmitting**~~ clock synchronous signals, base band information, transmission information and master control information among BBUs**, and** to achieve interconnection and data sharing among BBUs.

46. (previously presented) The Base band unit according to Claim 45, wherein the interface unit further comprises at least one of:

a reset interface for resetting the base station;

an identification interface for marking the type of the base station and the position of the BBU;

a power supply switches for controlling power on and power off for itself;

a test interface for connecting with external test equipments;

a signal input interface for receiving external clock signals;

a Dry Contact input interface for expanding input Dry Contact functions of the

base station;

an electrostatic discharge (ESD) connector; and

a protect ground (PGND) terminal.

47. (previously presented) The Base band unit according to Claim 45, wherein the capacity expansion interface comprises one or a plurality of capacity expansion interface(s) providing the active/standby switchover control signal.

48. (previously presented) The Base band unit according to Claim 46, wherein the signal input interface comprises at least one of a signal input interface for receiving GPS synchronous clock signals and a signal input interface for receiving 2M synchronous clock signals.

49. (previously presented) The Base band unit according to Claim 46, wherein the test interface comprises at least one of a 10M test interface for outputting 10M test synchronous clock signals and a transmission time interval (TTI) test interface for outputting TTI signals.

50. **(canceled)**

51. (previously presented) The Base band unit according to Claim 39, wherein the Main Processing & Timing unit, the base band signal processing unit, the transmission unit and the interface unit are integrated on a single board.